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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
09/842,750	04/25/2001	B. Arlen Young	ADPT1048	8262
7590	06/17/2004			EXAMINER FAROOQ, MOHAMMAD O
Forrest Gunnison Gunnison, McKay & Hodgson, L.L.P. Suite 220 1900 Garden Road Monterey, CA 93940			ART UNIT 2182	PAPER NUMBER 7
DATE MAILED: 06/17/2004				

Please find below and/or attached an Office communication concerning this application or proceeding.

<b>Office Action Summary</b>	<b>Application No.</b>	<b>Applicant(s)</b>
	09/842,750	YOUNG, B. ARLEN
	<b>Examiner</b>	<b>Art Unit</b>
	Mohammad O. Farooq	2182

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

**Period for Reply**

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If the period for reply specified above is less than thirty (30) days, a reply within the statutory minimum of thirty (30) days will be considered timely.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

**Status**

1) Responsive to communication(s) filed on 05 April 2004.

2a) This action is **FINAL**.                            2b) This action is non-final.

3) Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

**Disposition of Claims**

4) Claim(s) 1-21 is/are pending in the application.

4a) Of the above claim(s) \_\_\_\_\_ is/are withdrawn from consideration.

5) Claim(s) 7-15 is/are allowed.

6) Claim(s) 1-5, 16-19 and 21 is/are rejected.

7) Claim(s) 6 and 20 is/are objected to.

8) Claim(s) \_\_\_\_\_ are subject to restriction and/or election requirement.

**Application Papers**

9) The specification is objected to by the Examiner.

10) The drawing(s) filed on 25 April 2001 is/are: a) accepted or b) objected to by the Examiner.

Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).

Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).

11) The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

**Priority under 35 U.S.C. § 119**

12) Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).

a) All    b) Some \* c) None of:

1. Certified copies of the priority documents have been received.
2. Certified copies of the priority documents have been received in Application No. \_\_\_\_\_.
3. Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

\* See the attached detailed Office action for a list of the certified copies not received.

**Attachment(s)**

1) Notice of References Cited (PTO-892)

2) Notice of Draftsperson's Patent Drawing Review (PTO-948)

3) Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08)  
Paper No(s)/Mail Date \_\_\_\_\_

4) Interview Summary (PTO-413)  
Paper No(s)/Mail Date. \_\_\_\_\_

5) Notice of Informal Patent Application (PTO-152)

6) Other: \_\_\_\_\_

**DETAILED ACTION**

***Claim Objections***

1. Claim 20 is objected to because of the following informalities: claim 20 is a duplicate of claim 18 under the same independent claim. Appropriate correction is required.

***Claim Rejections - 35 USC § 102***

The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless –

(a) the invention was known or used by others in this country, or patented or described in a printed publication in this or a foreign country, before the invention thereof by the applicant for a patent.

2. Claims 1-5, 16-19 and 21 are rejected under 35 U.S.C. 102(a) as being anticipated by Keaveny et al., U.S. Pat. No. 6,065,087.

3. As to claim 1, Keaveny et al. teach method for transmitting Packetized (since bus have widths of 8, 16 and 32 bits for data) SCSI Protocol command blocks comprising:

transmitting a first (i.e. beginning of transactions; col. 4 lines 54-61) Packetized SCSI Protocol command block (col. 4, lines 62-67); and  
transmitting a second Packetized SCSI Protocol command block with a substantially zero latency following transmission of said first Packetized SCSI Protocol command block (since bottlenecks and inefficiencies are eliminated, providing greater throughput; col. 2, lines 36-54).

4. As to claim 2, Keaveny et al. teach method wherein transmitting said first Packetized SCSI Protocol command block further comprises:

transmitting at least one byte in said first Packetized SCSI Protocol command block directly from a storage location of said at least one byte (inherent; col. 4, lines 40-67).

5. As to claim 3, Keaveny et al. teach method wherein said storage location of said at least one byte is within a stored first hardware I/O control (i.e. I/O command) block (col. 4, lines 42-53).

6. As to claim 4, Keaveny et al. teach method wherein is a logical unit number (LUN) of said first Packetized SCSI protocol command block (inherent; col. 6, line 55- col. 7, line 23).

7. As to claim 5, Keaveny et al. teach method comprising a pointer register (to hold pointers for storage location; fig. 3B; col. 7, lines 1-23).

8. As to claim 16, Keaveny et al. teach a Packetized SCSI Protocol hardware packet engine comprising:

a hardware information unit transfer controller having a start input line and a data out phase input line (inherent) wherein the hardware information unit transfer controller sequence hardware generation of a Packetized SCSI command blocks upon receipt of an active signal on the start input line and an active signal on the data out phase input line (col. 6, lines 35-54; col. 7, line 61- col. 8, line 60); and

a hardware header generator coupled to the hardware information unit transfer controller (since it generates header; fig. 3A-3C), wherein the header generator generated fields in a command L\_Q information unit in response to signals from the hardware information unit transfer controller (col. 7, lines 1-23, 61-67; col. 8, lines 1-60).

9. As to claim 17, Keaveny et al. teach hardware packet engine further comprising a hardware body generator coupled to the hardware information unit transfer controller, wherein the body generator generates fields in a command information unit in response to signals from the hardware information unit transfer controller (inherent, since the system asserts signals such as ATN; col. 6, lines 15-54).

10. As to claims 18 and 19, Keaveny et al. teach the Packetized SCSI Protocol hardware packet engine comprising a hardware I/O control block pointer register (to hold pointers; fig. 3B; col. 7, lines 1-23) coupled to the hardware header generator and hardware body generator (inherent).

11. As to claim 21, Keaveny et al. teach method for generating a Packetized SCSI Protocol command block comprising:

transferring information required in a command information unit and available in a hardware I/O control block directly from the hardware I/O control block (col. 7, lines 1-67); and

transferring information required in the command information unit but unavailable (BUS FREE state) in the hardware I/O control block directly from a register (inherent; col. 7, lines 1-67).

***Response to Arguments***

12. Applicant's arguments filed April 4, 2004 have been fully considered but they are not persuasive.

13. The examiner disagrees with the applicant prior art Keaveny et al. do not teach "timing/latency" and "packetized."

Since the reference teaches eliminating inefficiencies and bottlenecks, and increasing throughput. In order to achieve these objectives for a system, the timing would be affected and therefore, would have to reduce the latency close to zero as the applicant claims.

Further, the reference teaches the SCSI buses have 8, 16 and 32 bits of width and the transfer of data or commands through this bus would have to meet the required number of bits and therefore, these data or commands could be called or considered as packets of data or commands.

***Allowable Subject Matter***

14. Claims 7-15 are allowed.

15. Claim 6 is objected to as being dependent upon a rejected base claim, but would be allowable if rewritten in independent form including all of the limitations of the base claim and any intervening claims.

16. **THIS ACTION IS MADE FINAL.** Applicant is reminded of the extension of time policy as set forth in 37 CFR 1.136(a).

A shortened statutory period for reply to this final action is set to expire THREE MONTHS from the mailing date of this action. In the event a first reply is filed within TWO MONTHS of the mailing date of this final action and the advisory action is not mailed until after the end of the THREE-MONTH shortened statutory period, then the shortened statutory period will expire on the date the advisory action is mailed, and any extension fee pursuant to 37 CFR 1.136(a) will be calculated from the mailing date of the advisory action. In no event, however, will the statutory period for reply expire later than SIX MONTHS from the mailing date of this final action.

17. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Mohammad O. Farooq whose telephone number is (703) 305-3888. The examiner can normally be reached on 8:00am - 4:30pm.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Jeffrey A. Gaffin can be reached on (703) 308-3301. The fax phone number for the organization where this application or proceeding is assigned is (703) 746-7239.

Any inquiry of a general nature or relating to the status of this application or proceeding should be directed to the receptionist whose telephone number is (703) 305-3900.



JEFFREY GAFFIN  
SUPERVISORY PATENT EXAMINER  
TECHNOLOGY CENTER 2100

Mohammad O. Farooq  
June 10, 2004